

**EXPEDITED PROCEDURE AFTER FINAL REJECTION  
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: Walter Fix et al.

Serial No.: 10/562,869

Filed: April 7, 2006

For: Logic Gate with a Potential-Free Gate Electrode for Organic Integrated Circuits

Examiner: Eva Y. Montalvo Art Unit: 2814

Attorney Dkt: 411000-144 Customer No. 27162

**SUPPLEMENTAL RESPONSE UNDER 37 CFR 1.116**

MS RCE  
Commissioner for Patents  
Box 1450  
Alexandria, VA 22313-1450

This paper accompanies an RCE and is supplemental to a prior response to the Office Action dated Dec. 16, 2008 filed on Feb. 27, 2009. This paper is also in response to an Advisory Action dated March 25, 2009. The Office Action has a one month extended time for response set to expire on April 16, 2009 wherein applicants hereby request a one month extension of time to respond to the Action of Dec. 16, 2008. The Commissioner is authorized to charge deposit account 03 0678 the \$130 extension fee and the \$810 RCE fee for this paper or a total of \$940 and any additional fees that might be due for this paper or credit any over payments.

**IN THE CLAIMS**

Amend the claims as shown starting on page 2 of this paper.

Remarks begin on page 4.